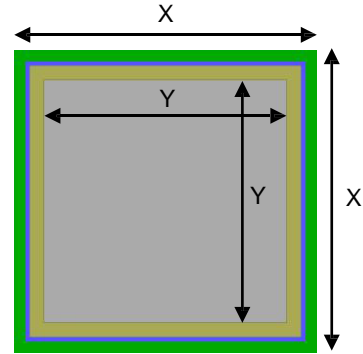




## Single Layer Capacitor (SLC)

### Feature

- High breakdown voltage and very low leakage current.
- Suitable for the application of TO-Can package.
- Gold pad metal for easy wire bonding.



### Electrical Characteristics

PARAMETER	SPECIFICATION	TEST CONDITIONS
Temperature Coefficient of Capacitance	0.01%/°C	-40°C ~ +125°C
Capacitance	220pF~10nF	V = 5V, f = 100KHz
Capacitance Tolerance	±20%	
Breakdown Voltage	≥ 100V	@ Leakage Current 10nA

### Physical Specifications

PARAMETER	UNIT	SPECIFICATION
Chip Size (X)	μm <sup>2</sup>	500x500, 750x750
Chip Size Tolerance	μm	±20
Chip Height*	μm	150, 250, 275, 350, 400
Chip Height Tolerance	μm	±20
Bonding Area (Y)	μm <sup>2</sup>	415x415, 620x620
Bonding Area Tolerance	μm	-5 ~ +15

\* Modifiable

### Package Type

Expanded wafer on tape.

### Ordering Information

P/N: SLC-E 561 E 250

<b>Size Code</b> C: 0.500 x 0.500 mm <sup>2</sup> E: 0.750 x 0.750 mm <sup>2</sup> G: 1.0 x 1.0 mm <sup>2</sup>	<b>Thickness</b> 150: 150μm 200: 200μm 250: 250μm 300: 300μm 350: 350μm 400: 400μm
<b>Nominal Capacitance</b> EIA code: 471: 470pF 561: 560pF 102: 1000pF 222: 2200pF	<b>Breakdown Voltage</b> A: 12V B: 25V C: 50V D: 75V E: 100V